

Exhibit 8

AT&T – Smartphone (See product list and end for models) Infringement of the '790 patent	
Claim 1	Evidence
1. An interface for receiving data from an image sensor having an imaging array and a clock generator for transfer to a processor system comprising:	For example, an image capturing subsystem of the AT&T smartphone has a CMOS image sensor that includes an imaging array and a clock generator. An image processing subsystem of the AT&T smartphone includes a processor that processes image data. The AT&T smartphone includes interface circuitry that receives image data from the image capturing subsystem and transfers the image data to the processor. The interface circuitry thereby enables the transfer of image data between the image capturing subsystem, which runs in a pixel clock domain, and the image processing subsystem, which runs in a processor clock domain.
a memory for storing imaging array data and clocking signals at a rate determined by the clocking signals;	The AT&T smartphone provides a memory for storing imaging array data and clocking signals at a rate determined by the clocking signals.
	For example, the interface circuitry of the AT&T smartphone includes a buffer module that stores the image data that is received from the image capturing subsystem. The buffer module has control and clock signal inputs. The buffer module clocks its internal and external signals at a rate that is determined by the input clock signals. This enables the buffer module to store the image data at a rate that is in accordance with the pixel clock domain of the image capturing subsystem.
a signal generator for generating a signal for transmission to the processor system in response to the quantity of data in the memory; and	The AT&T smartphone provides a signal generator for generating a signal for transmission to the processor system in response to the quantity of data in the memory.
	For example, the interface circuitry of the AT&T smartphone includes interface functionality that generates a signal when the buffer module has

<p>a circuit for controlling the transfer of the data from the memory at a rate determined by the processor</p>	<p>image data that is ready for transmission to the processor. The signal indicates that the buffer module has a frame or sub-frame of image data for the processor.</p> <p>The AT&T smartphone provides a circuit for controlling the transfer of the data from the memory at a rate determined by the processor system.</p> <p>For example, the interface circuitry of the AT&T smartphone includes timing and control functionality that controls the transfer of image data from the buffer module to the processor. The timing and control functionality enables the image data to be transferred at a rate determined by the processor system. This enables the processor to acquire the image data at a rate that is in accordance with the processor clock domain.</p>
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Product List
 Cingular Flip IV
 Radiant Max, Radiant Core
 Fusion Z

References:

- [1] AT&T Cingular Flip IV
<https://www.att.com/buy/phones/att-cingular-flip-4-4gb-black.html>
- [2] AT&T Cingular Flip IV Review
<https://www.pcmag.com/reviews/att-cingular-flip-iv>
- [3] AT&T Cingular Flip™ IV User Guide
<https://www.att.com/idpassets/images/support/device-support/ATT-U102AA-UG-EN-V9b-MR.pdf>
- [4] Shop for prepaid phones
<https://www.att.com/buy/prepaid-phones/browse/att>
- [5] Radiant Max
<https://www.att.com/buy/prepaid-phones/att-radiant-max-3.2gb-cobalt-blue-prepaid.html>
- [6] Radiant Core

<https://www.att.com/buy/prepaid-phones/att-radiant-core-16gb-dark-grey-prepaid.html>

[7] Fusion Z

<https://www.att.com/buy/prepaid-phones/att-fusion-z-prepaid.html>